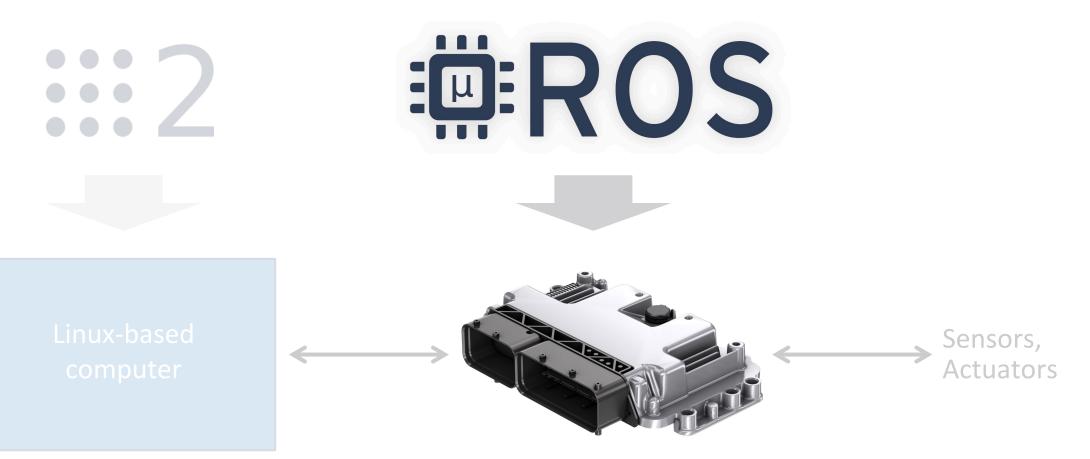
ROSCon 2022

micro-ROS goes Automotive: supporting AUTOSARbased microcontrollers

Jan Staschulat and Ralph Lange, Bosch Research





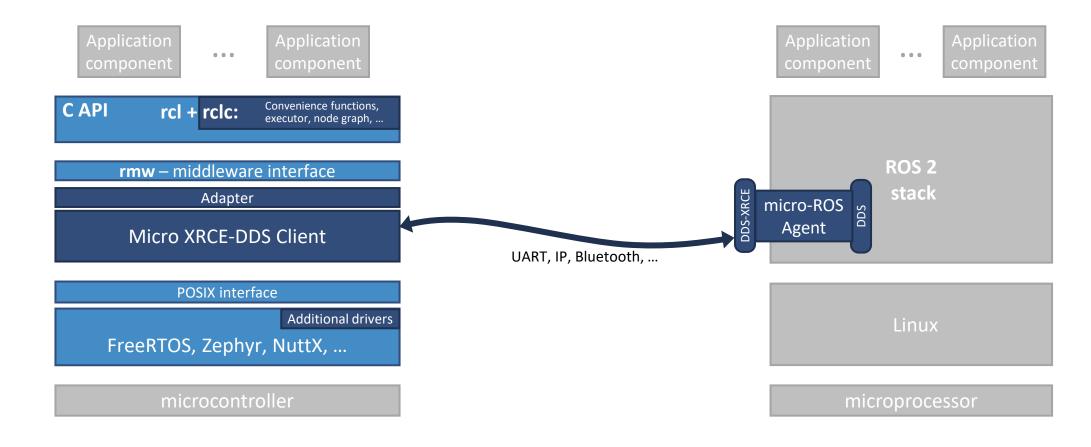
300 MHz Clock Frequency

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- 1 MB SRAM
- AUTOSAR Classic

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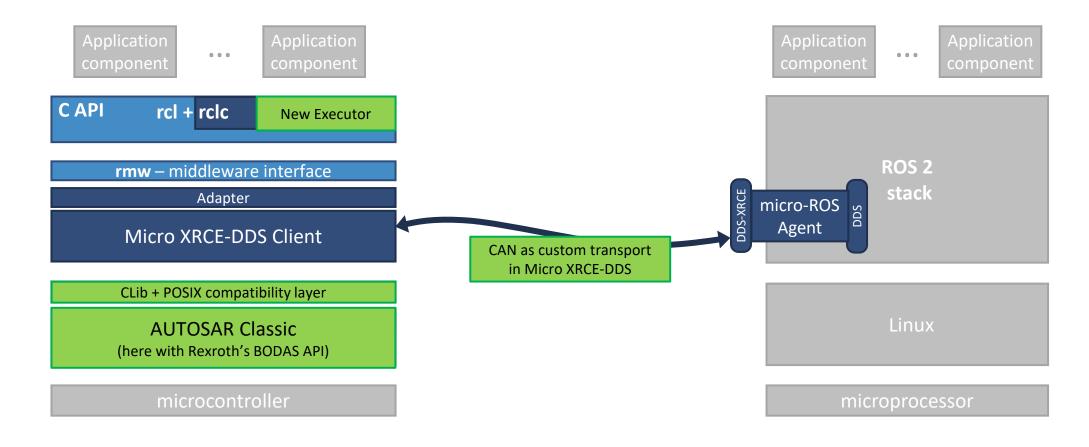
Micro-ROS Architecture



Jan Staschulat (CR/ADI1.2) and Ralph Lange (CR/AD CE-RSE) | 2022-10-21



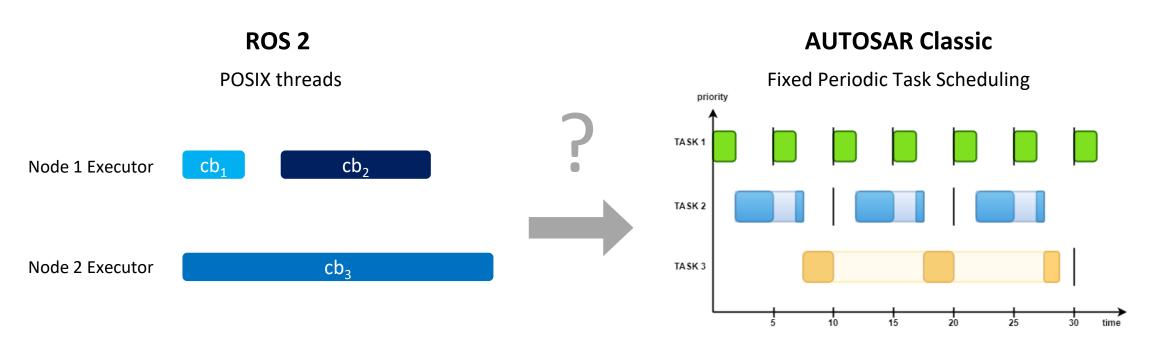
Modified Architecture



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Challenge 1: Mapping ROS Execution to AUTOSAR Tasks



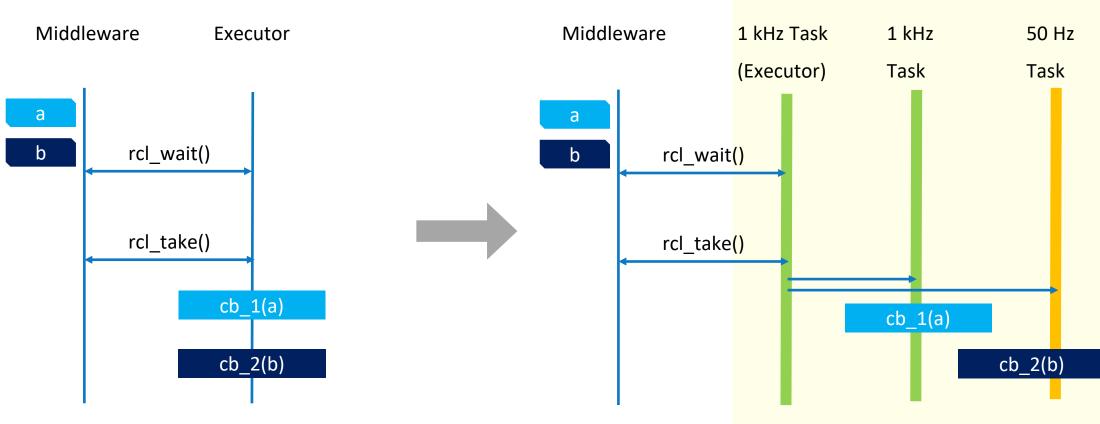
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Solution: Dispatching Executor

ROS 2

AUTOSAR Classic

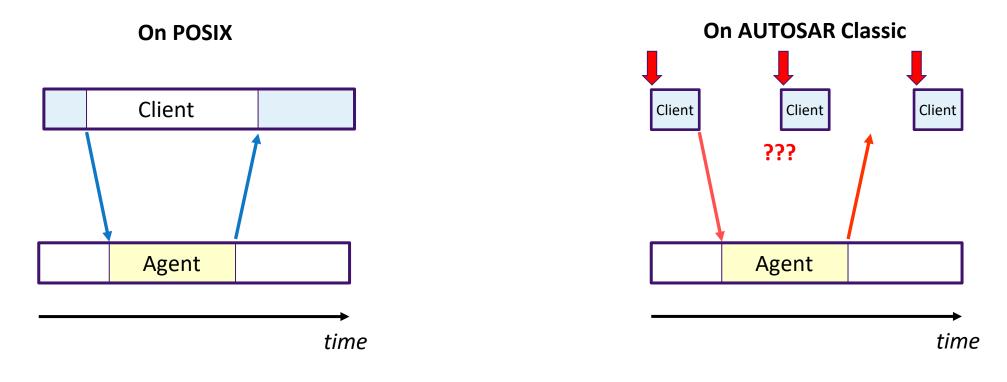


Available in github.com/ros2/rclc (for Rolling)

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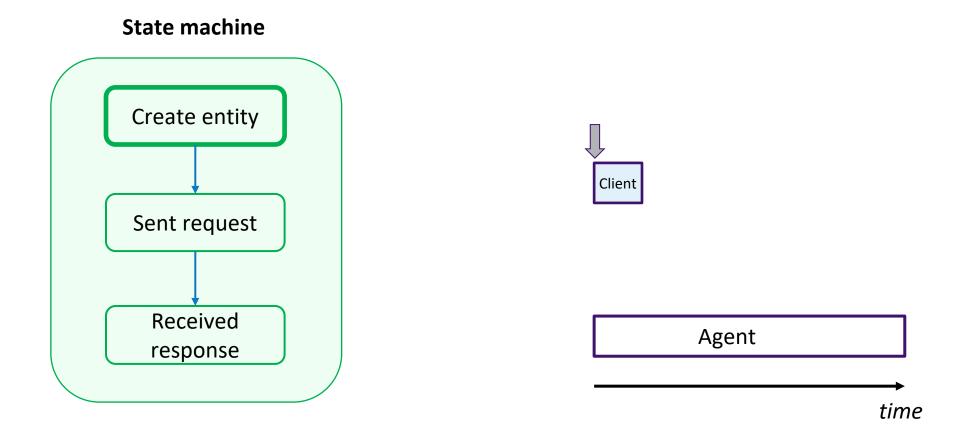
Challenge 2: Handling blocking initializations in rcl and rmw



Client thread waits for Agent's reply

Periodic tasks cannot wait in stack

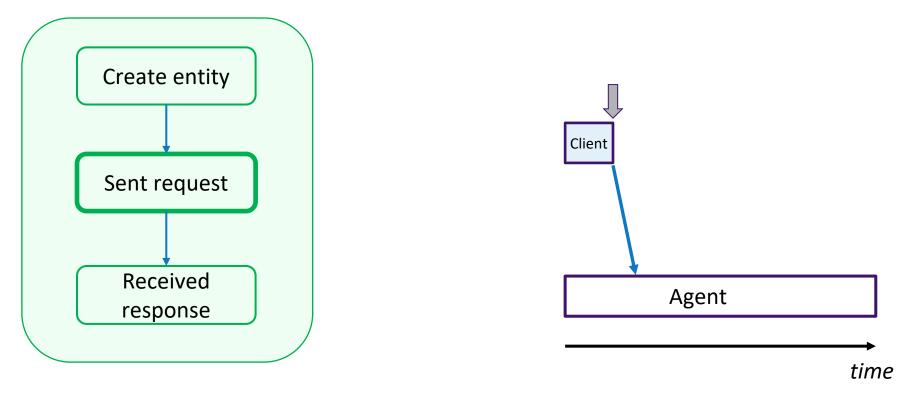




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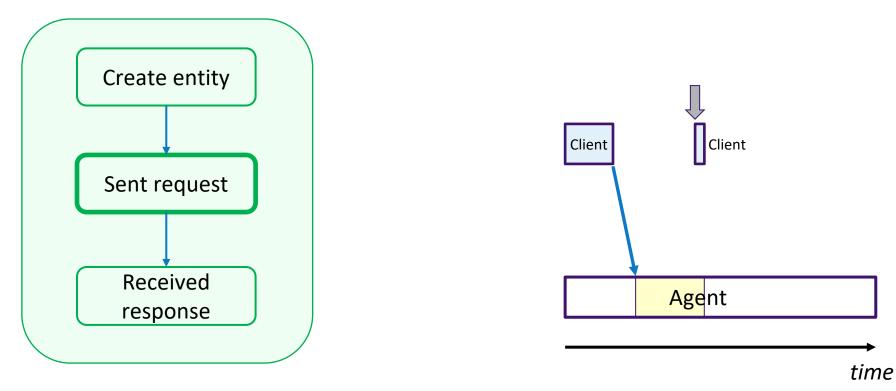




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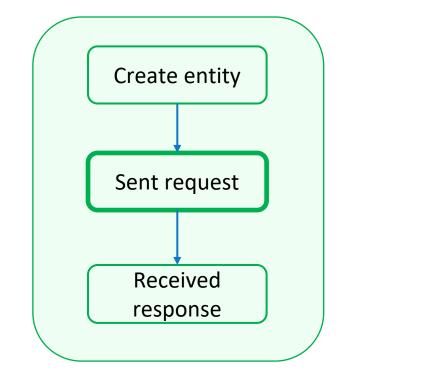
State machine

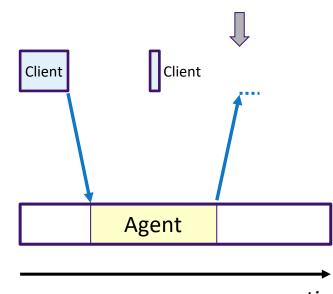


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State machine



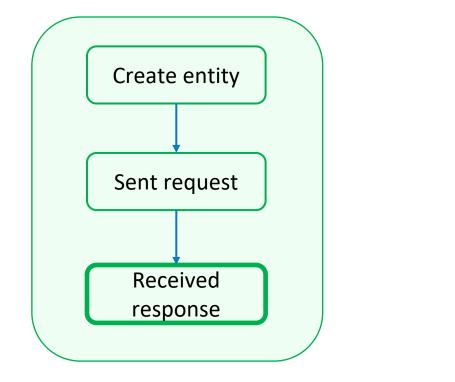


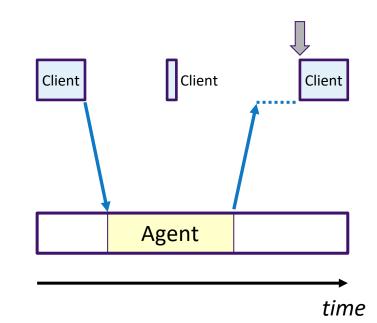


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State machine

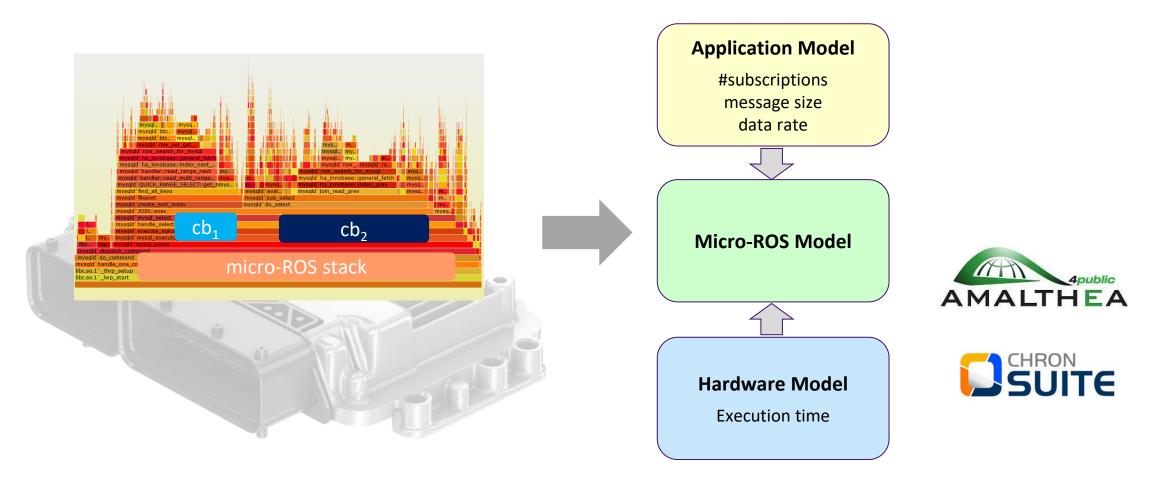




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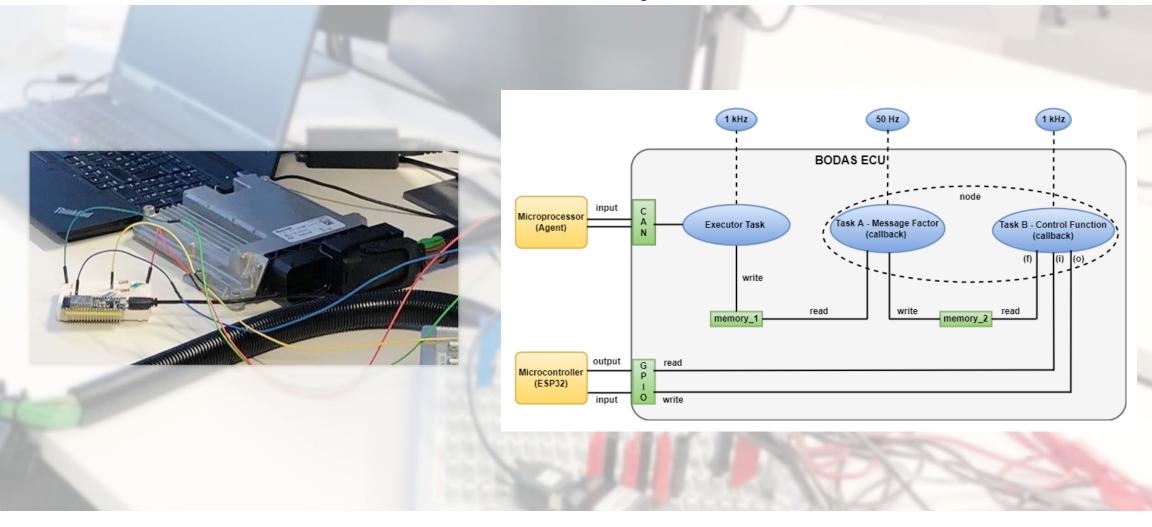
How to model the performance of a micro-ROS application?



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Demonstrator with BODAS Controller by Bosch Rexroth

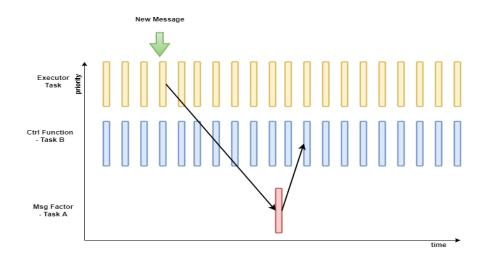


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Results

- Runtime overhead by micro-ROS stack is < 3%
 - ... despite quick user code callbacks
- Model estimates response times with error of ≈6%



	Real HW	Model
min latency [us]	970	1029
avg latency [us]	10320	n/a
max latency [us]	19978	20070

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Ported micro-ROS stack to AUTOSAR-based platform

- rclc Dispatching Executor to map execution model
- Refactored blocking initializations into state machines
- Support for classical CAN as custom transport
- micro-ROS performance model in Amalthea
- Many thanks to Kaiwalya Kalyan Belsare and Suraj Rao Bappanadu for the contributions in their Master's theses!

Learn more?

- Jan.Staschulat@de.bosch.com | github.com/JanStaschulat
- Ralph.Lange@de.bosch.com | github.com/ralph-lange

... also see upcoming chapter "micro-ROS" in Springer's ROS Book Vol. 7





micro-RO

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